Qn1. Private data that is used by a single-processor, then shared data are used by

1. Single processor
2. Multi processor
3. Single tasking
4. Multi tasking

Conversion of this hexa expression eca8 6420hex into binary is

1. 0000 1100 1010 1000 0110 0100 0010 0000two
2. 1110 1100 1010 1000 0110 0100 0010 1111two
3. 1110 1100 1010 1000 0110 0100 0010 0000two
4. 1110 1100 1010 1000 0110 0111 0010 0000two

Load instruction has a delay or latency that cannot be eliminated by forwarding, other technique used is

1. **Pipeline interlock**
2. Deadlock
3. Stall interlock
4. Stall deadlock

In distributed system each processor has its own

a) local memory  
b) clock  
**c) both local memory and clock**  
d) none of the mentioned

Which routing technique is used in distributed system?  
a) fixed routing  
b) virtual routing  
c) dynamic routing  
**d) all of the mentioned**

Inter process communication :  
a) allows processes to communicate and synchronize their actions when using the same address space  
**b) allows** processes to communicate and synchronize their actions without using the same address space  
c) allows the processes to only synchronize their actions without communication  
d) none of the mentioned

CPU fetches the instruction from memory according to the value of  
**a) program counter**  
b) status register  
c) instruction register  
d) program status word

Which one of the following is the address generated by CPU?  
a) physical address  
b) absolute address  
**c) logical** address  
d) none of the mentioned

Program always deals with  
a) **logical address**  
b) absolute address  
c) physical address  
d) relative address

The page table contains  
a) **base address of each page in physical memory**  
b) page offset  
c) page size  
d) none of the mentioned

Operating System maintains the page table for  
**a)** each process  
b) each thread  
c) each instruction  
d) each address

Because of virtual memory, the memory can be shared among  
**a) processes**  
b) threads  
c) instructions  
d) none of the mentioned

Swap space exists in  
a) primary memory  
**b)** secondary memory  
c) cpu  
d) none of the mentioned

In FIFO page replacement algorithm, when a page must be replaced  
**a) oldest** page is chosen  
b) newest page is chosen  
c) random page is chosen  
d) none of the mentioned

Two main types of branch instructions are

1. conditional branch
2. unconditional branch
3. logical branch
4. **both a and b**

A source program is usually in \_\_\_\_\_\_\_  
a) Assembly language  
b) Machine level language  
**c)** High-level language  
d) Natural language

Which memory device is generally made of semi-conductors?  
a) **RAM**  
b) Hard-disk  
c) Floppy disk  
d) Cd disk

The control unit controls other units by generating \_\_\_\_  
a) Control signals  
**b)** Timing signals  
c) Transfer signals  
d) Command Signals

. \_\_\_\_\_\_ bus structure is usually used to connect I/O devices.  
**a)** Single bus  
b) Multiple bus  
c) Star bus  
d) Rambus

. \_\_\_\_\_\_ is generally used to increase the apparent size of physical memory.  
a) Secondary memory  
**b) Virtual memory**  
c) Hard-disk  
d) Disks

The addressing mode, where you directly specify the operand value is \_\_\_\_\_\_\_  
a) Immediate  
**b)** Direct  
c) Definite  
d) Relative